

Title: METHOD AND APPARATUS FOR MULTIPLE BYTE
OR PAGE MODE PROGRAMMING AND READING AND
FOR ERASING OF A FLASH MEMORY ARRAY

Inventor(s): Kyung Joon Han et al.

Docket No.: 11188.00

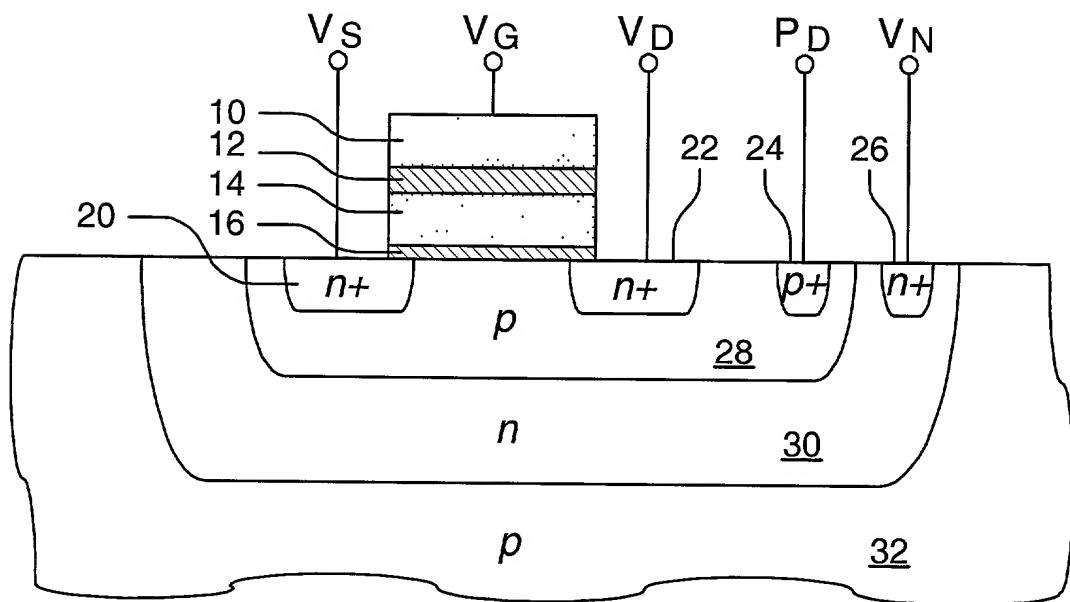


FIG. 1 (PRIOR ART)

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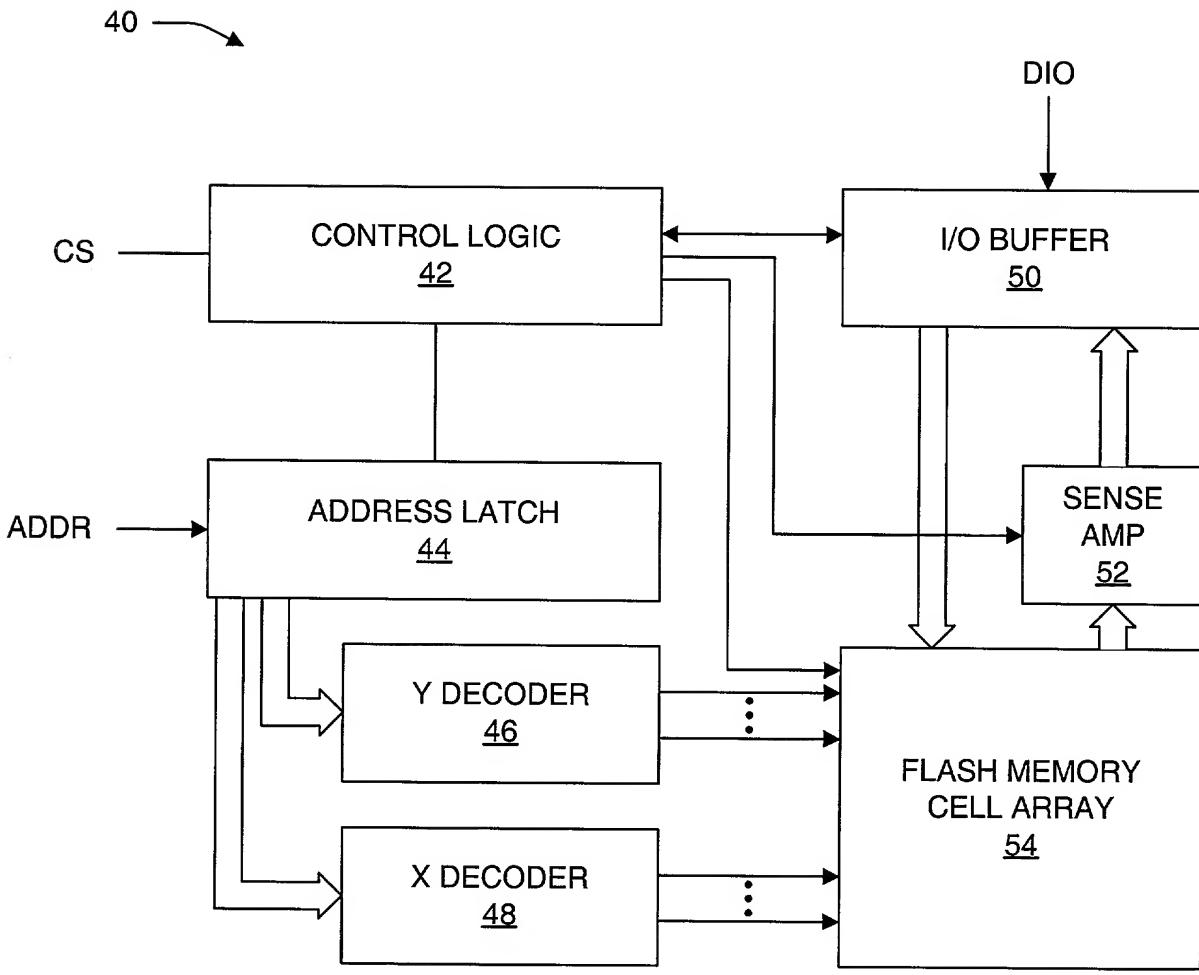


FIG. 2 (PRIOR ART)

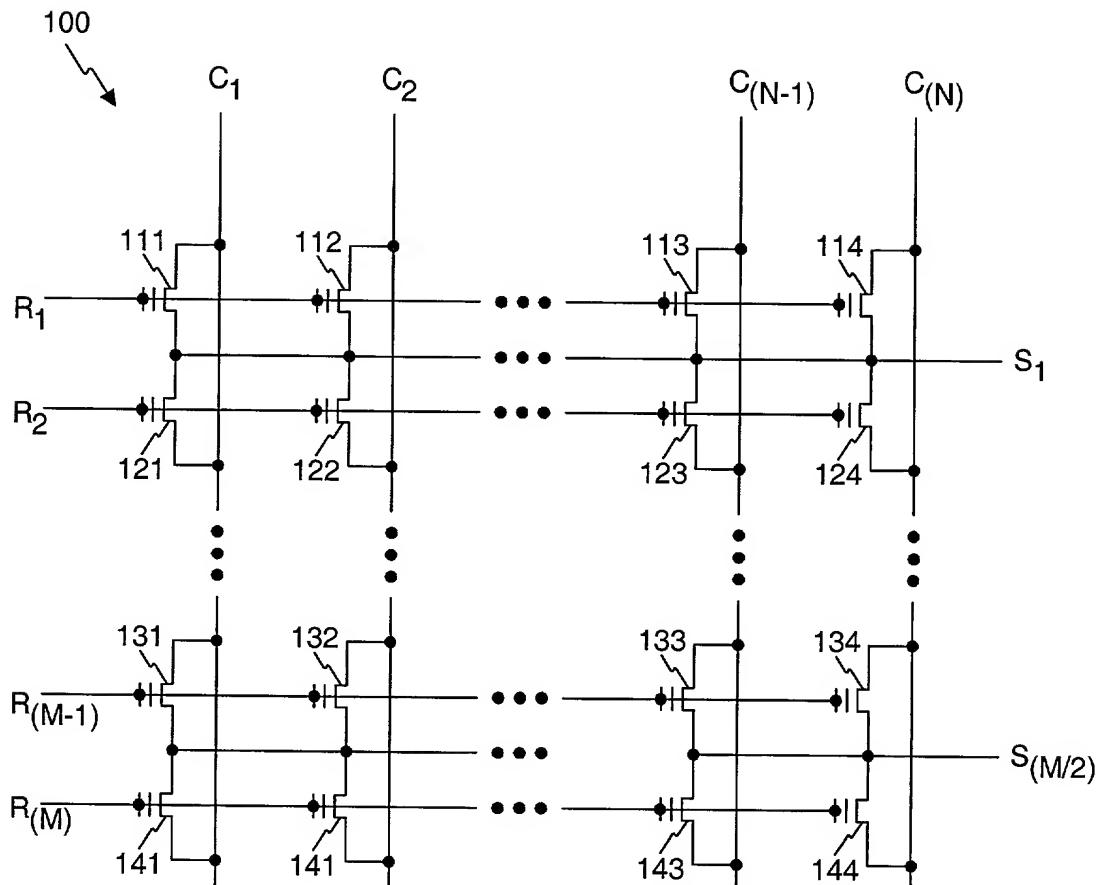


FIG. 3

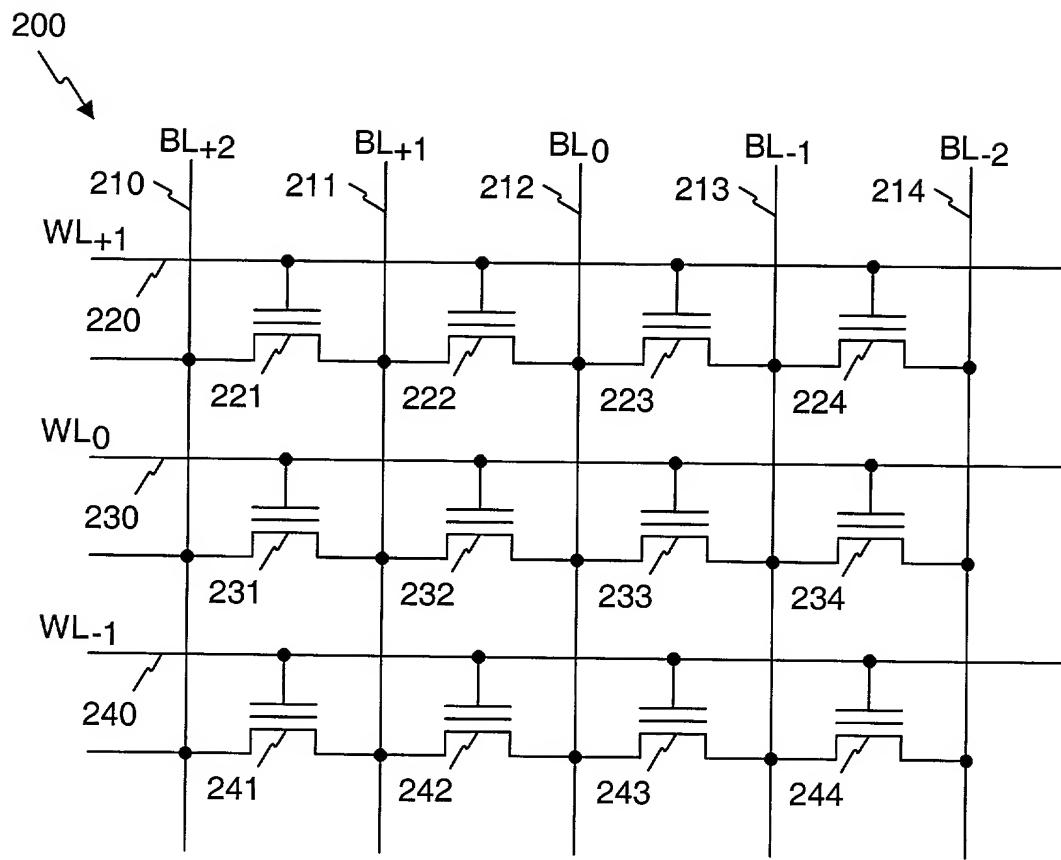


FIG. 4

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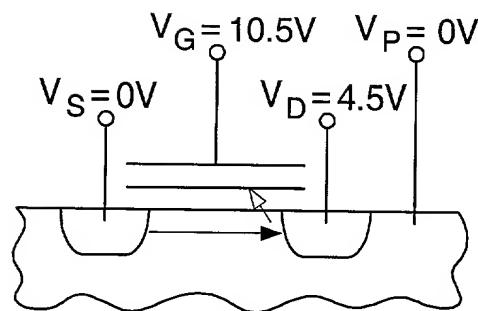


FIG. 5

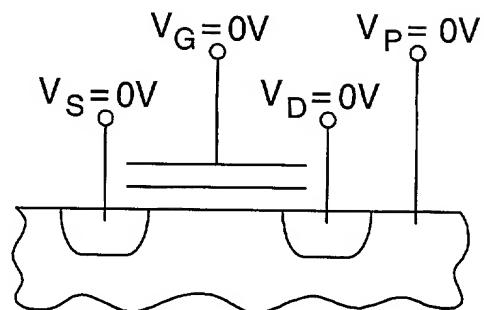


FIG. 6

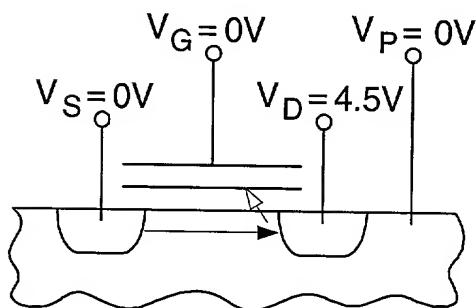


FIG. 7

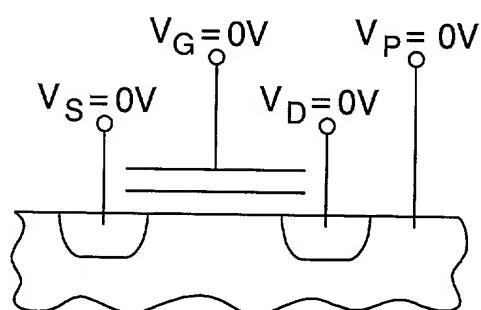


FIG. 8

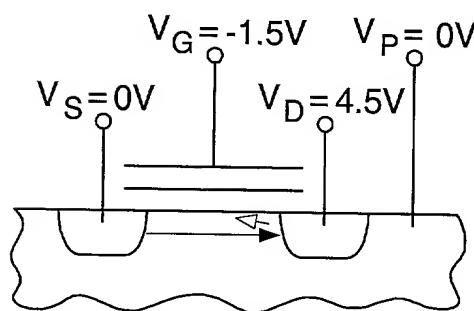


FIG. 9

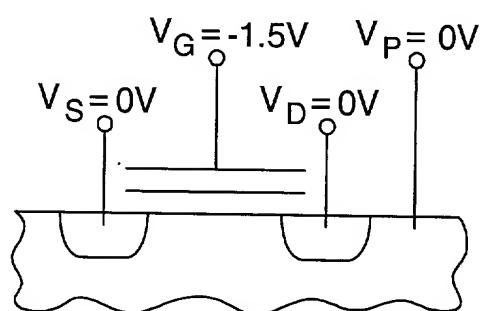


FIG. 10

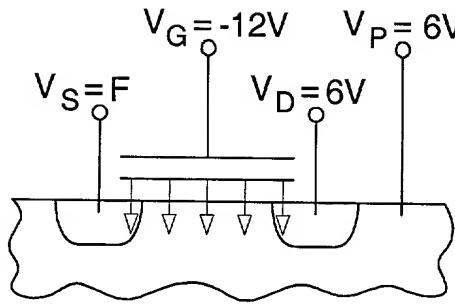


FIG. 11

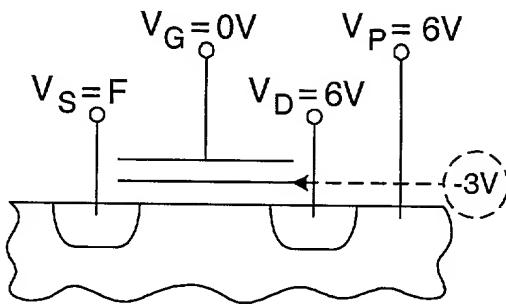


FIG. 12

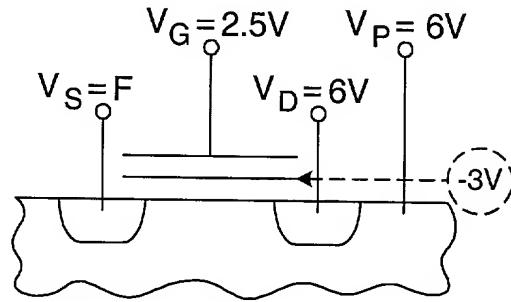


FIG. 13

OPERATION	V_G SELECT	V_G UNSEL	V_S	V_D	V_P	V_{NW}	V_T
READ	2.5V TO 3V	-1.5V	0V	1V TO 1.5V	0V	0V	
PAGE PROGRAM	10V TO 10.5V	-1.5V	0V	4.5V	0V	0V	5.5V
PAGE ERASE	-11V TO -12V	2.5V TO 3V	F OR 6V	6V	6V	6V	2V

FIG. 14

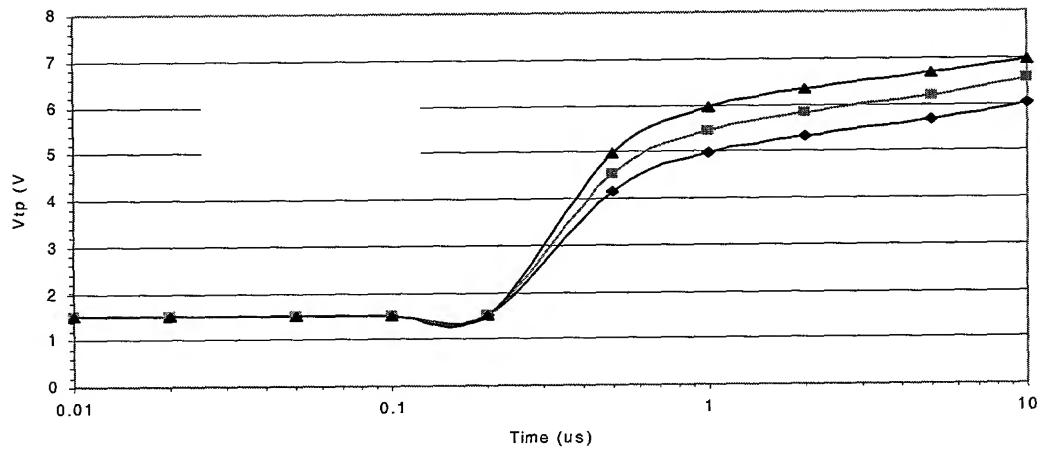


FIG. 15

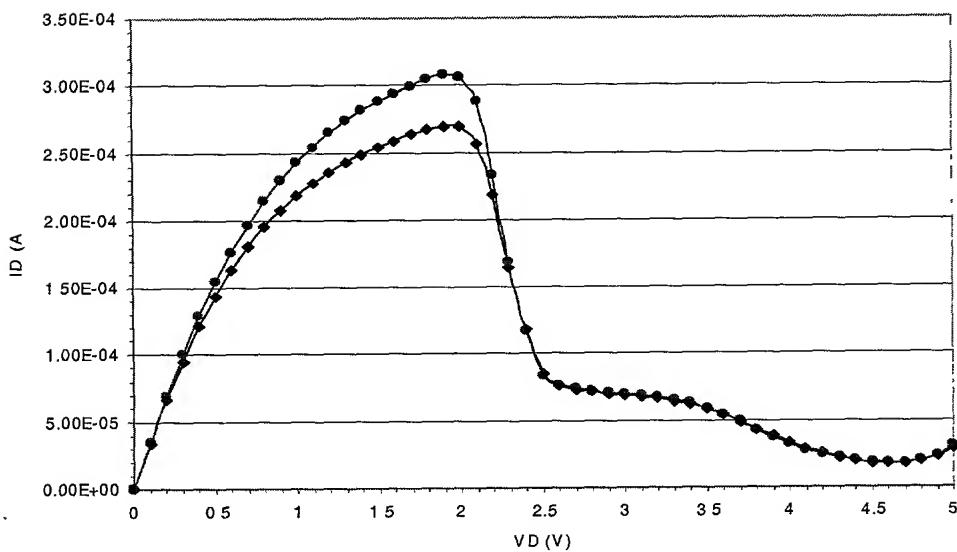


FIG. 16

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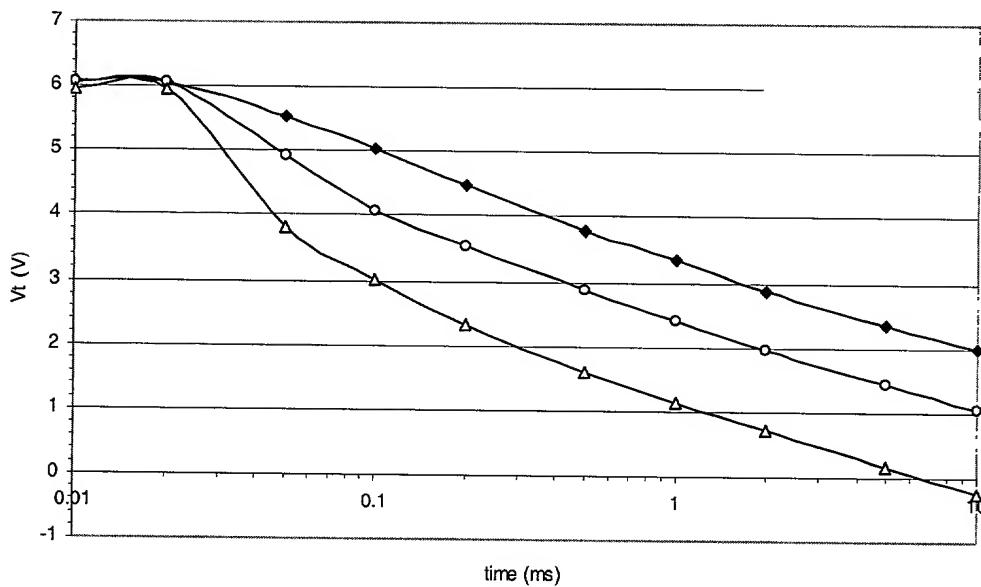


FIG. 17

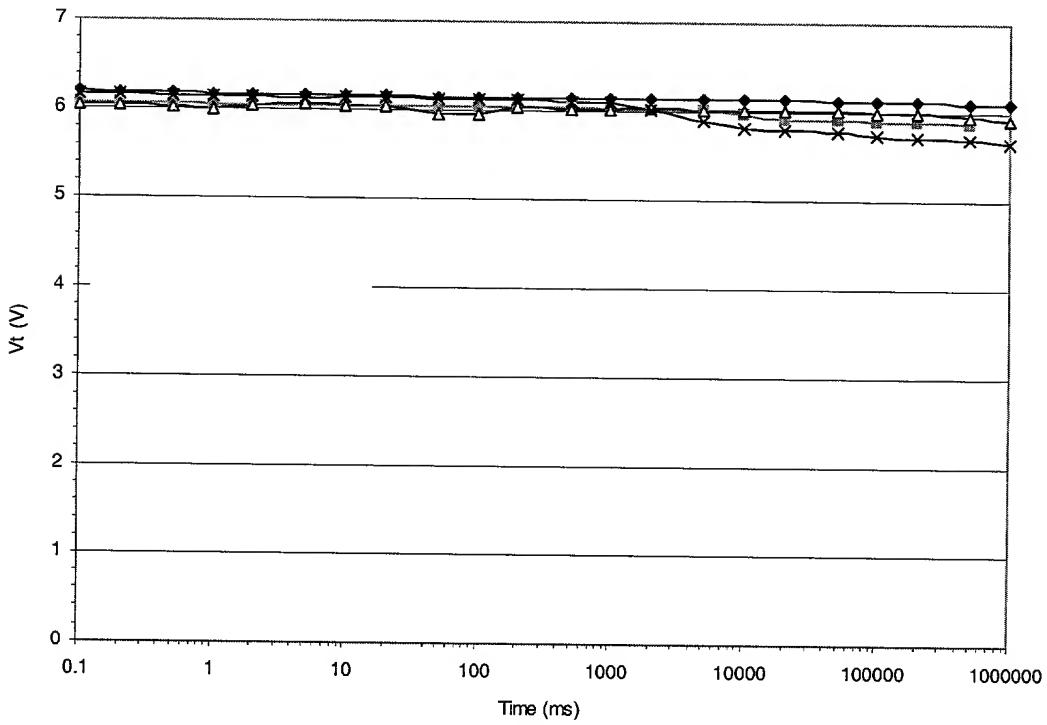


FIG. 18